1. (Currently amended) A system ~~System~~ for driving rows of a liquid crystal display ~~characterised in that it comprises~~ comprising:

at least one module [[(10)]] for driving one single row of said liquid crystal display, said module comprising an inverter [[(T11-T12)]] operating in a supply path between a first [[(21)]] and a second [[(22)]] supply line of said system, said first supply line [[(21)]] comprising first means [[(S1)]] capable of coupling ~~connecting~~ it to a first [[(VLCD)]] or to a second [[(VA)]] supply voltage and said second supply line [[(22)]] comprising second means [[(S2)]] capable of coupling ~~connecting~~ it to a third [[(VB)]] or to a fourth [[(VSS)]] supply voltage, said inverter [[(T11-T12)]] being driven by [[a]] logic circuitry [[(11-12)]] and providing ~~sending in output (OUT)~~ a drive signal for one single row of said liquid crystal display.

2. (Currently amended) The system ~~System~~ according to claim 1, wherein ~~characterised in that~~ said inverter comprises ~~(T11-T12) is made up of~~ a PMOS transistor [[PMOS]] and a NMOS transistor [[NMOS]].

3. (Currently amended) The system ~~System~~ according to claim 1, wherein ~~characterised in that~~ the value of said first supply voltage [[(VLCD)]] exceeds said second supply voltage [[(VA)]], the value of said second supply voltage [[(VA)]] exceeds said third supply voltage [[(VB)]], and the value of said third supply voltage [[(VB)]] exceeds said fourth supply voltage [[(VSS)]].

4. (Currently amended) The system ~~System~~ according to claim 1, wherein ~~characterised in that~~ said first [[(S1)]] and second [[(S2)]] means are controlled by a logic signal [[(F)]] that controls respectively the connection of the first supply line [[(21)]] to said first [[(VLCD)]] or to said second [[(VA)]] supply voltage and the connection of the second supply line [[(22)]] to said third [[(VB)]] or to said fourth [[(VSS)]] supply voltage according to whether a [[the]] frame is uneven or even.

5. (Currently amended) The system ~~System~~ according to claim 4, wherein ~~characterised in that~~ said logic circuitry [[(11-12)]] comprises a logic device [[(11)]] capable of supplying an additional input logic signal [[(A)]] to an elevator device capable of raising the level of said additional logic signal

[[(A)]] for driving said inverter [[(T11-T12)]].

6. (New) A module for driving a row in a liquid crystal display comprising:

an inverter having first and second power terminals;

a first switch for coupling the first power terminal of the inverter to a first or a second supply voltage; and

a second switch for coupling the second power terminal of the inverter to a third or fourth supply voltage, wherein the inverter is driven by a logic circuit and provides a drive signal for the row.

7. (New) The module of claim 6, wherein the inverter comprises a PMOS transistor and a NMOS transistor.

8. (New) The module of claim 6, wherein the first and second supply voltages have different values, and the third and fourth supply voltages have different values.

9. (New) The module of claim 6, wherein the first and second switches are driven by a logic signal, the state of the logic signal being determined by whether a frame is uneven or even.

10. (New) The module of claim 9, further comprising a level shifter.